

Application No.: 10/605,571

Docket No.: 22040-00020-US

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

Claims 1-6: (Canceled).

7. (Previously Presented) A multistaged amplification circuit comprising:

a differential amplification circuit, equipped with n differential amplifiers that are connected in a multistaged manner and that amplify and output an input signal from a previous stage to a following stage;

a plurality of transistors connected to said n differential amplifiers that are connected in a multistaged manner, and connected to one constant current source by a current mirror; and

a plurality of separate ground lines,

wherein each of said plurality of transistors is connected to a common ground node by a dedicated one of the plurality of separate ground lines.

8. (Previously Presented) The multistaged amplification circuit of claim 7, wherein the plurality of separate ground lines collectively suppress a distributed voltage drop.

9. (Currently amended) A multistaged amplification circuit, comprising:

a differential amplification circuit, equipped with n differential amplifiers that amplify and output an input signal from a previous stage to a following stage, and that are connected in a multistaged manner;

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a plurality of transistors connected to a plurality of differential amplifiers and one constant current by a current mirror for every group into which said n plurality of differential amplifiers are divided; and

means for suppressing a ground line distributed voltage drop associated with each of the plurality of transistors,

wherein the means for suppressing a distributed voltage drop comprises a plurality of separate ground lines.

wherein each of said plurality of transistors is connected to a common ground node by a dedicated one of the plurality of separate ground lines.

10. (Previously Presented) The multistaged amplification circuit of claim 9, wherein the means for suppressing a ground line distributed voltage drop reduces electrical noise.

11. (Previously Presented) The multistaged amplification circuit of claim 9, wherein the means for suppressing a ground line distributed voltage drop preserves linearity of an amplified signal.

12. (Previously Presented) The multistaged amplification circuit of claim 9, wherein the means for suppressing a ground line distributed voltage drop stabilizes a high frequency operating characteristic of the multistaged amplification circuit.

13. (Previously Presented) The multistaged amplification circuit of claim 9, wherein the means for suppressing a ground line distributed voltage drop reduces electrical noise, preserves linearity of an amplified signal, and stabilizes a high frequency operating characteristic of the multistaged amplification circuit.

14. (Canceled).